JTRS AIM

A member of General Dynamics’ Family of Advanced Core Cryptographic Technologies (FAC2T)

Overview

The JTRS AIM builds on 40 years of fielded Type 1 cryptographic chips, modules, and communication products. It is the next generation successor to the Advanced INFOSEC Machine (AIM), which has seen wide use in both General Dynamics products and those of other top tier communications providers. The JTRS AIM enhances many of AIM’s proven features like total programmability and adds new capabilities to ease integration into new developments. Investments in current AIM-based platforms are leveraged since the JTRS AIM is code compatible with AIM. Like AIM, the JTRS AIM is a state-of-the-art INFOSEC device that serves as the core of a secure system.

JTRS AIM

JTRS AIM is a programmable, embeddable security engine for communications equipment requiring high-grade cryptographic processing. The state-of-the-art JTRS AIM chip provides a secure hardware platform on which software-based cryptographic algorithms and higher-level crypto equipment applications (CEAs) can execute. JTRS AIM is totally programmable and supports interoperability with legacy equipment as well as modern net-centric systems. It enables the products in which JTRS AIM is embedded to be modified or upgraded in the future with a download of software. The JTRS AIM design includes three independent cryptographic processors that are tailored to efficiently execute key management and traffic encryption/decryption functions. The AIM II programmable cryptographic module (part of JTRS AIM) is certified by the National Security Agency (NSA) to secure information classified up to and including Top Secret, Sensitive Compartmentalized Information (SCI) for the Joint Tactical Radio System (JTRS) Handheld, Manpack, and Small Form Fit (HMS) and Airborne Maritime Fixed (AMF) radios.
JTRS AIM — Embeddable Programmable Security

JTRS AIM Features
- Faster context switching (one AIM/JTRS AIM can be used in place of multiple competitors’ devices)
  - Enhanced traffic engines support one active and three shadow programs
  - Single clock context switching between four different CEAs (reduced latency)
- Efficiently meets new security requirements
- Enhanced algorithm performance
- Two parallel ports and two serial ports per Interface Processor (improved support for MLS systems)
- Support for multiple Red Processors
- New Command and Control Interface
  - Supports new security requirements with fewer parts
- New programmable chip selects and general purpose I/O
- Reduces external support parts
- Supports legacy and future crypto modernization waveforms
- Backward compatibility with existing AIM software
- Improved power, size and environmental characteristics
  - 0.13 micron processing technology
  - 1.2 volt core
  - –40°C to 85°C
- Non-CCI prior to Type 1 programming

JTRS AIM Applications
- Software definable radios
- Single- and multi-channel radios
- Type 1 and non-Type 1 radios
- Multiple Level Security radios and Network Interface Cards (NIC) HAIPE
- Legacy crypto replacement
- Crypto Modernization programs
- Homeland security applications
- Avionic (manned and unattended) applications
- JTRS radio products (e.g., marine, vehicle, manpack, handheld, small form fit, airborne, munitions)
- Key management products and applications modules

AIM and JTRS AIM Algorithm and Crypto Equipment Application (CEA) Software
The algorithms and CEAs shown below have been developed to support AIM and JTRS AIM.

Algorithms
- Accordion
- Acme
- AES (AIM)
- Baton
- Benign Techniques
- Crayon
- DES, 3-DES
- Digital Signature Algorithm (DSA)
- Elliptic Curve Cryptographic (ECC)
- Elliptic Curve Signature Algorithm (ECDSA)
- Firefly
- Jackknife
- Joseki
- Keesee
- Mark XII (Cadmus)
- Medley
- Phalanx
- Saville
- SHA-1/256/384/512
- Shillelagh
- Vallor
- Walburn
- Weasel

CEAs
- APCO 25
- CCSA
- FED
- HAIPE (Taclane, KG-235)
- Havequick I/II
- IFF Mode 4
- IFF Mode 5
- JPALS*
- KG-84A/C (KIV-7)
- KGR-96
- KGV-8
- KGV-10
- KGV-11
- KVR-46
- KY-57/58
- KY-99/100
- KYV-5 (ANDVT)
- NES
- Saturn*

* In development